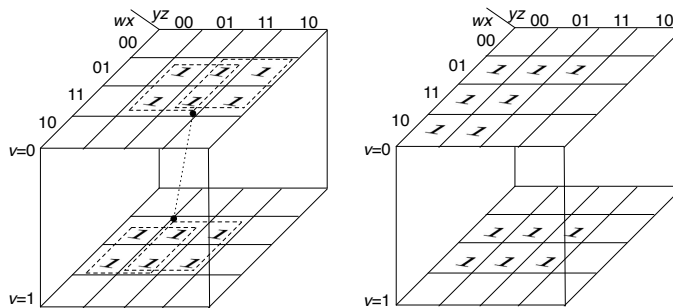


Assigned: Tuesday 8 September

Due: Monday 14 September 10:30 pm

Problems

- In Logisim, use basic logic gates (AND, OR, and NOT) to build a 2-to-4 decoder.
- In Logisim, use basic logic gates (AND, OR, NOT) to build a 6-input multiplexor. Note that a multiplexor always has exactly one output.
- Five-variable Karnaugh maps can be visualized by stacking two four-variable maps as follows.

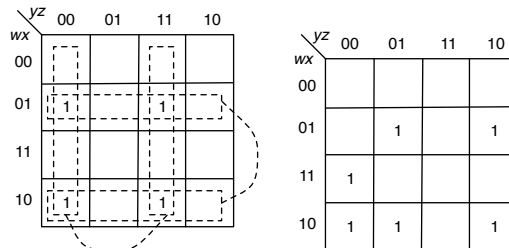


The groups on the left map give rise to the reduced function $f(v, w, x, y, z) = xz + \bar{v}xy + vx\bar{y}$.

- Give a minimized function $g(v, w, x, y, z)$ for the map on the right.
 - Draw a PLA for f and g together in the style of Patterson and Hennessy Figure C.3.5 (p. C-16).
- Give a minimized function for the following map with don't cares marked as X.

	$\backslash yz$	00	01	11	10
wx	00	1	X		
	01		1	X	
	11	X	1	1	
	10		1	X	X

- Karnaugh maps can be extended to other forms by recognizing that the AND operation requires an intersection of regions. For example, the intersection of two disconnected regions in the map on the left gives the function $f(w, x, y, z) = (w\bar{x} + \bar{w}x) \cdot (yz + \bar{y}\bar{z})$. Use the same idea to find a reduced form of the function in the map on the right.



What to turn in

- For Logisim problems, turn in files as `problem1.circ` and `problem2.circ`. Also include these as images in your PDF. To get image files, choose `File > Export Image` and choose the PNG format.
- The PLA may be neatly hand drawn and scanned. All other items must be typeset.
- To receive credit, all problems must be visually included, numbered, and ordered, in a single PDF.

Acknowledgments

Problems 1 and 2 are by Janet Davis, used under the [Creative Commons Attribution-Noncommercial-Share Alike 3.0 United States License](#). Problems 3, 4, and 5 are adapted from Hohn, Figures 4.7.1 (p. 190), 4.9.9 (p. 200), 4.9.8 (p. 199), 4.9.2 (p. 195), and 4.9.3 (p. 195).¹

¹ Hohn, E. F. (1966). *Applied Boolean Algebra* (Second Edition). Macmillan: New York.